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Wordlength Optimization for Custom Floating-point Systems

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Introduction



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INTERR Increase and impact of computer systems



Extracted from [1]. A scenario of normalized emissions projections for computer systems over the year

- Environmental impact of computer systems increase and will increase over the years
- Impact come from production, deployment and usage
- Proposed solution :
 - Reduce die size
 - Reduce operational energy

=> A lead : Use of approximate computing



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[1]"Kaya for Computer Architects: Toward Sustainable Computer Systems", Lieven Eeckhout, IEEE Micro, Volume 43, 2023

Introduction

Optimization flow

Proposed strategies

Experiments

A lead : Approximate computing





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A lead : Approximate computing



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Approximate computing

 $\frac{\text{Fixed point}}{\text{x* } 2^{-\text{E}} = (-1)^{\text{S}} * \text{Int}}$

Floating point

$$x=(-1)^{S} * (1 + M) * 2^{E-\Delta}$$



- Exponent implicit in the code

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- Exponent explicit in the coded value



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Floating-point : a new contender ?

		Picojoules per operation	
Operation		45nm	7nm
+	Int 32	0.1	0.03
	IEEE FP 32	0.9	0.38
Х	Int 32	3.1	1.48
	IEEE FP 32	3.7	1.31
SRAM	8KB	10	7.5
	32KB	20	8.5
	1MB	100	14

Data extracted from [2] Energy per operation for a 45nm and 7 nm systems

- Fixed point was first choice thanks to low operational energy
- Floating point operational energy reduces over the year (~ - 2X)
- Memory usage is the higher energy requirement now
- Floating point can lead to lower memory usage
- => Custom floating-point systems became an option for embedded systems



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[2]"Ten Lessons From Three Generations Shaped Google's TPUv4i", Norman P. Jouppi et al, ISCA 2021

Introduction

Optimization flow



Challenge :

Determine a generic automatic wordlength optimization method for a given accuracy



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Experiments



Outline :

I.Introduction

II.Optimization flow

III.Proposed strategies

IV.Experiments



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Introduction



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Introduction



Source code



User source code : • Write in C/C++

 \circ Any algorithm







Source to source transformation :

- Write in C/C++
- \circ Any algorithm



Simple strategy



Wordlength optimization : MIN+1

Wordlength Optimization

MIN+1 algorithm:

- Greedy heuristic algorithm
- Two steps algorithm : Ο

1) Find the minimal value for each dimension that satisfy the criterion

Set all value to the minimal found

2) Increment the dimension with the best gradient until the criterion is met



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IETR Cost evaluation

Cost evaluation

Requirement :

- Types of memory
- Energy for a given operation and wordlength Ο
- Architectures Ο



Quality evaluation

Quality evaluation







Simple strategy



Optimization process :

- o **Input** : $(W_{E_{max}}, W_{E_{max}}, ..., W_{E_{max}}, W_{M_{max}}, W_{M_{max}}, ..., W_{M_{max}})$
- $\circ \lambda_{min}$: The user minimal quality



IETR Optimization time







Challenge :

Determine a generic **automatic** wordlength optimization method for a given accuracy in a **reasonable amount of time**



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Floating-point description



$$x=(-1)^{S} * (1 + M) * 2^{E-\Delta}$$















Exponent bits required



Number of exponents bits required :

$$W_{E_{Ri}} = \left\lceil \log_2 \left(R_{E_v} \right) \right\rceil + 1$$



Strategy 1: Exponent and mantissa wordlength simultaneous optimization







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Strategy 1: Exponent and mantissa wordlength simultaneous optimization

Expected pros

- Explore a lot of useful configuration
- Explore all dimensions



Expected cons

- Still a huge configuration number required :

$$n_p = M^{N_v} * \prod_{i=0}^{N_v} w_{E_i}^{\max}$$

- Huge Optimization time



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Strategy 2: Mantissa wordlength only optimization



Number of exponents bits required :

$$W_{E_{Ri}} = \left[\log_2 \left(R_{E_v} \right) \right] + 1$$



Strategy 2: Mantissa wordlength only optimization



Only mantissa wordlengthWith : \checkmark \circ Input : $(W_{M_{max}}, W_{M_{max}}, ..., W_{M_{max}})$ \circ λ_{min} : The user minimal quality \circ All exponent size set to $W_{E_{Ri}}$



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ETR Strategy 2 : Mantissa wordlength only optimization

Expected pros

Small configuration number required :

 $n_p = M^{N_v}$

Smaller Optimization time

Expected cons

Doesn't try to reduce the exponent wordlength _



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Strategy 3 : Exponent and mantissa wordlength sequential optimization



With :

- o **Input 1**: $(W_{E_{R1}}, W_{E_{R2}}, ..., W_{E_{RN}})$
- \circ λ_{min_1} : First user minimal quality
- \circ All mantissa size set to $W_{M_{max}}$
- \circ Input 2: $(W_{M_{max}}, W_{M_{max}}, ..., W_{M_{max}})$
- $\circ \lambda_{min_2}$: Second user minimal quality
- All exponent size set to W_{E_i} obtained with first optimization



Strategy 3 : Exponent and mantissa wordlength sequential optimization

Expected pros

Middle ground for configuration number :

 $n_p = M^{N_v} + \prod_{i=0}^{N_v} w_{E_i}^{\max}$

- Middle ground for the result quality



Expected cons

- Required and dependent of an hyperparameters : λ_{min_1}



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Experiment 1: Infinite Impulse Response filter

- Quality metric : Signal to Quantization Noise Ratio (SQNR)
- \circ Number of variable to optimize : 7





Experiment 2: Squeezenet

- Quality metric : proportion of similar top 1 classes detected
- Number of variable to optimize : 15



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Power improvement : Infinite Impulse Response filter





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Power improvement : Squeezenet



Optimization flow

Proposed strategies

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- Proposed :
 - New custom floating-point refinement
 - Three strategies to optimize exponent and mantissa wordlength
 - Allow to follow a user-defined quality constraint
- Find improvement in terms of power and memory consumption compared to half floatingpoint
- Perspective :
 - Increase the quality of optimization
 - Implement memory cost reading/storing energy in power estimation

